

FAIRCHILD MICROCOMPUTERS

8-BIT BIPOLAR MICROPROCESSOR FAMILY

LSI PERIPHERAL LOGIC ELEMENTS

Item	DEVICE NO.	Functional Description	Power Supply V	Maximum Frequency MHz (Typ)	Power mW (Typ)	Logic/ Connection Diagram	Package(s)
1	9401	16-Bit Cyclic Redundancy Generator/Checker	5.0	18	350	P26	6A,7A
2	9403	16x4-Bit Serial/Parallel FIFO Buffer Memory	5.0	10	600	P27	6Q,9U
3	9423	64x4-Bit Serial/Parallel FIFO Buffer Memory	5.0	8.0	750	P27	6Q,9U

BIT SLICE MICROPROCESSORS

Item	DEVICE NO.	Functional Description	Power Supply V	Maximum Frequency MHz (Typ)	Power mW (Typ)	Logic/ Connection Diagram	Package(s)
4	9404	Data Path Switch	5.0	10	300	P28	6Q,9U
5	9405A	4-Bit Arithmetic Logic Register Stack (CPU slice with 8 Registers)	5.0	13	550	P29	6Q,9U
6	9406	16x4 push-down pop-up Program Stack	5.0	10	500	P30	6Q,9U
7	9407	Data Access Register (PC, SP and operand pointer)	5.0	10	450	P31	6Q,9U
8	9408	10-Bit Microprogram Sequencer/Controller (pipeline capability)	5.0	7.0	650	P32	6Q,9U
9	9408A	10-Bit Microprogram Sequencer/Controller (pipeline capability)	5.0	10	650	P32	6I,8P
10	9410	Register Stack (16x4 RAM with output latch)	5.0	25	375	P33	7D,9M

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